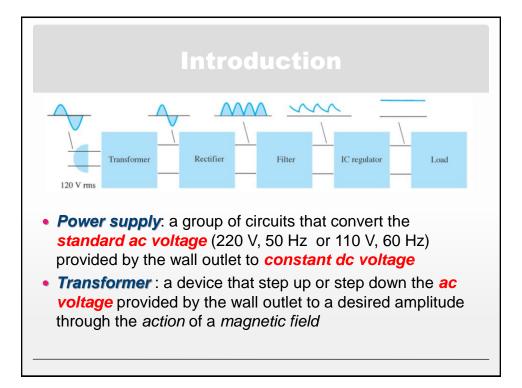
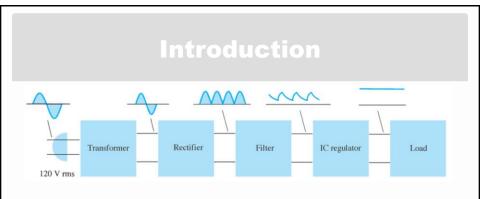
Voltage Regulator

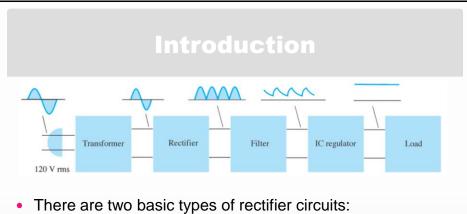
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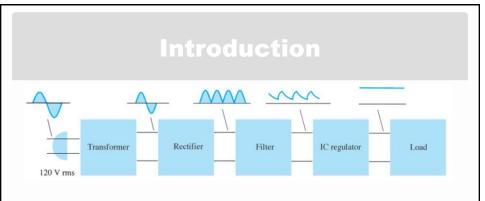
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- Rectifier: a diode circuits that converts the ac input voltage to a pulsating dc voltage
- The pulsating dc voltage is only suitable to be used as a battery charger, but not good enough to be used as a dc power supply in a radio, stereo system, computer and so on.



-
- Half-wave rectifier
- Full-wave rectifier Center-tapped & Bridge full-wave rectifier
- In summary, a full-wave rectified signal has less ripple than a half-wave rectified signal and is thus better to apply to a filter.

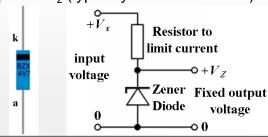


- Filter: a circuit used to reduce the fluctuation in the rectified output voltage or ripple. This provides a steadier dc voltage.
- Regulator: a circuit used to produces a constant dc output voltage by reducing the ripple to negligible amount.
 One part of power supply.

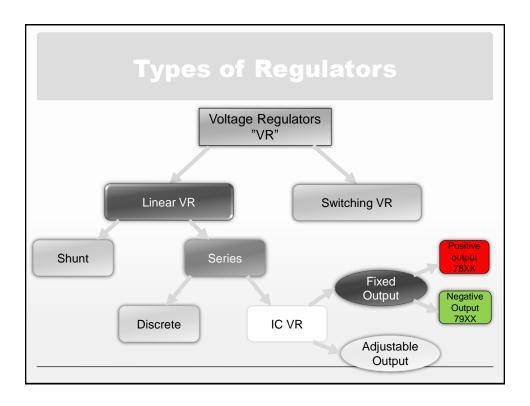
Voltage Regulators

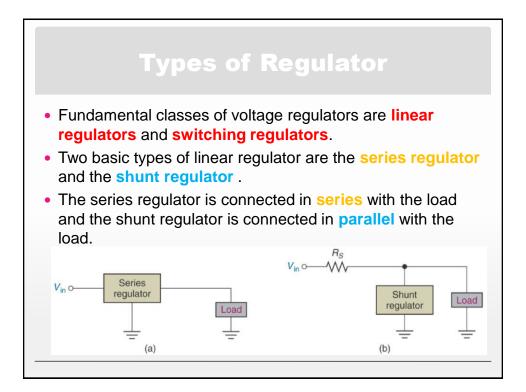
Regulator - Zener diode regulator

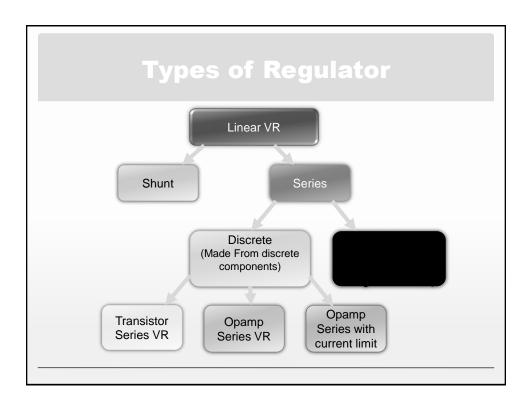
- For low current power supplies a simple voltage regulator can be made with a resistor and a zener diode connected in reverse.
- Zener diodes are rated by their breakdown voltage V_z and maximum power P_z (typically 400mW or 1.3W)

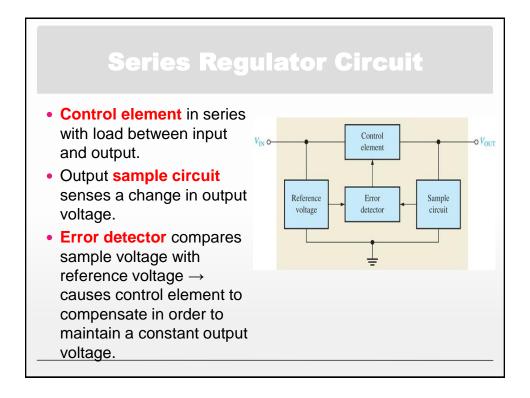


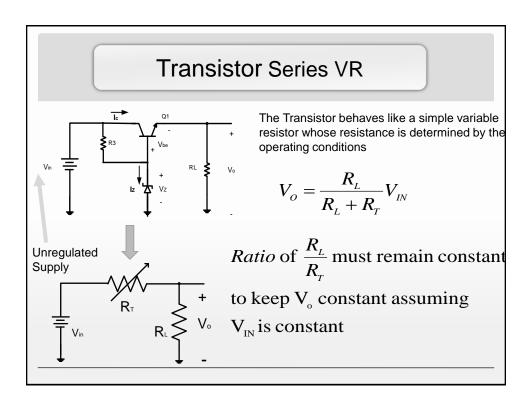
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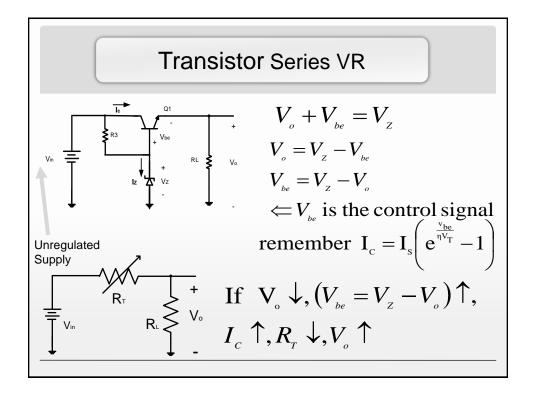






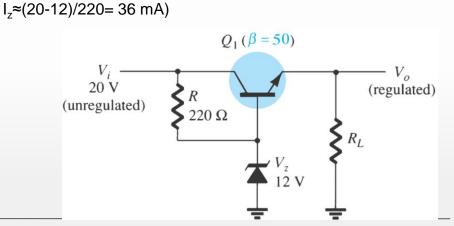


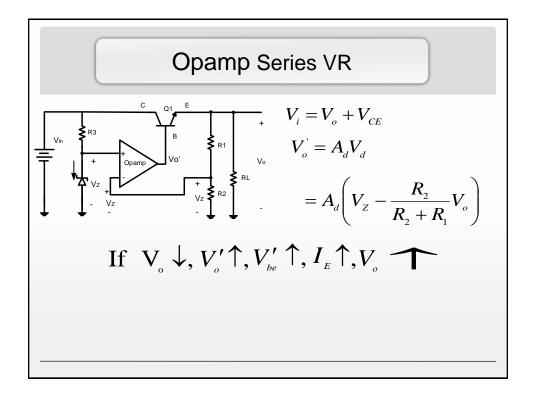


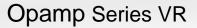


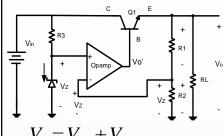


• Calculate the output voltage and Zener current for $R_L=1k\Omega$. (Solution: $V_o=Vz-Vbe=12-0.7=11.3\ V$;









Resistors R₁,R₂ are for sampling of Vo

(the current through these resistors must be small)

$$V_{o} = V_{R1} + V_{R2}$$
 $I_{R1} = I_{R2} = I$
 $V_{z} = V_{o} \frac{R_{2}}{R_{1} + R_{2}}$

$$V_{R2} = V_{Z}$$

$$= \frac{V_{Z}}{R_{2}}$$

$$V_{o} = V_{Z} \left(1 + \frac{R_{1}}{R_{2}} \right)$$

Summary of Op-Amp Series VR

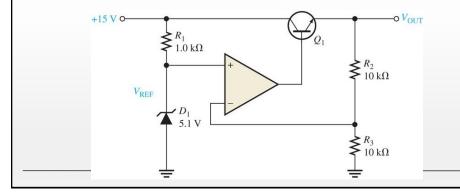
- The resistor R₁ and R₂ sense a change in the output voltage and provide a feedback voltage.
- The error detector compares the feedback voltage with a Zener diode reference voltage.
- The resulting difference voltage causes the transistor Q₁ to control the conduction to compensate the variation of the output voltage.
- The output voltage will be maintained at a constant value of:

$$V_o = \left(1 + \frac{R_1}{R_2}\right) V_Z$$

Example

• Determine the output voltage for the regulator below.

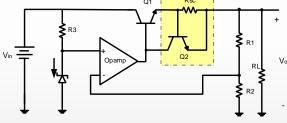
$$V_o = \left(1 + \frac{R_2}{R_3}\right)V_Z$$
 $V_o = \left(1 + \frac{10k}{10k}\right)5.1 = 10.2 \text{ V}$



Opamp Series VR with current limit

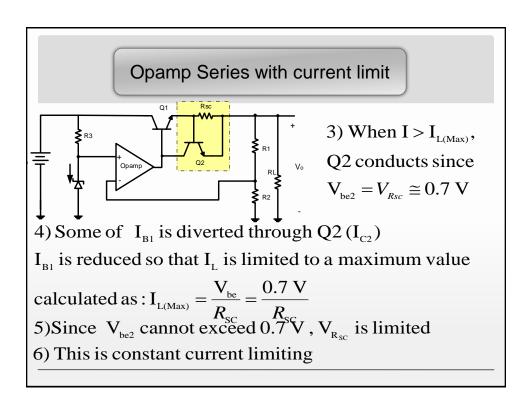
Current Limiting Circuit

In order to protect the transistor from damage when a very high current passes through it due to a short circuit or excessive current demand at the load



1) In normal operation Q2 is off since $V_{be2} = V_{Rsc} < 0.7 \text{ V}$

2)
$$R_{SC} = \frac{V_{be}}{I_{L(Max)}} = \frac{0.7 \text{ V}}{I_{L(Max)}}$$



Voltage Regulators example

- Given the following series voltage regulator
- 1) Complete the design of the following voltage regulator (Find of R1, R2 and R3) assuming that the voltage across the load resistor R₁ is equal to 12V. Assume Iz(min) = 2mA.
- 2)Show how to modify the circuit to limit the load current to 1A.
- 3)Find the output voltage for the modified circuit of part 2) when the load resistor $R_L = 100\Omega$ and when $R_L = 8\Omega$.

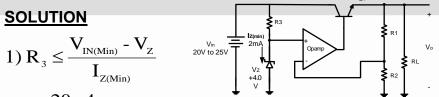
• 4) Choose a transistor with suitable power rating

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SOLUTION

$$1) R_3 \le \frac{V_{\text{IN(Min)}} - V_Z}{I_{\text{Z(Min)}}}$$



$$R_3 \le \frac{20-4}{2 \text{ mA}} = 8 \text{ k}\Omega$$
 in order to make sure $I_z > I_{Z(Min)}$

If $I_{Z(max)}$ was known, then lower limit for R3 can also be found

$$V_o = \left(1 + \frac{R_1}{R_2}\right) V_z = 12 \text{ V}$$

$$\therefore \frac{R1}{R2} = \frac{V_o}{V_z} - 1 = \frac{12}{4} - 1 = 2$$

$$\text{choose } R_1 = 20 \text{ k}\Omega$$

$$\therefore R_2 = 10 \text{ k}\Omega$$

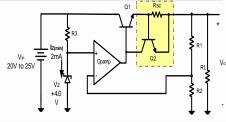
$$\rightarrow$$

$$\frac{1}{2} - 1 = \frac{12}{4} - 1 = 2$$

$$\frac{R1}{R2} = \frac{V_o}{V_z} - 1 = \frac{12}{4} - 1 = 2$$

Voltage Regulators

SOLUTION



2) — The change for current limit is done by adding Q2 and Rsc as shown

&
$$R_{SC} = \frac{V_{be}}{I_{L(Max)}} = \frac{0.7 \text{ V}}{1 \text{ A}} = 0.7 \Omega$$

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Ex. Continued

SOLUTION

For $R_L = 100$ ohm, Vo = 12V, then $I_L = \frac{12V}{100\Omega} = 0.12A$

which is smaller than $I_{L(max)}$, \therefore $V_o = 12$ V and is not affected by the current limit circuit

For
$$R_L = 8 \text{ ohm}$$
, $Vo = 12V$, then $I_L = \frac{12V}{8\Omega} = 1.5A$

which is bigger than $I_{\scriptscriptstyle L(max)},$ and the current limit circuit limits the current to the maximum allowable value which is 1 A

$$\therefore V_{O} = I_{L(Max)} * R_{L} = 1A * 8\Omega = 8 V$$

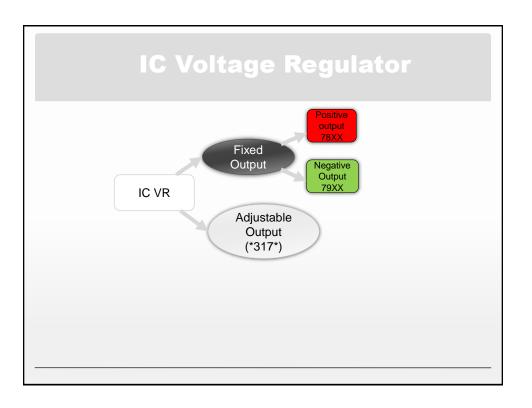
Example Continued

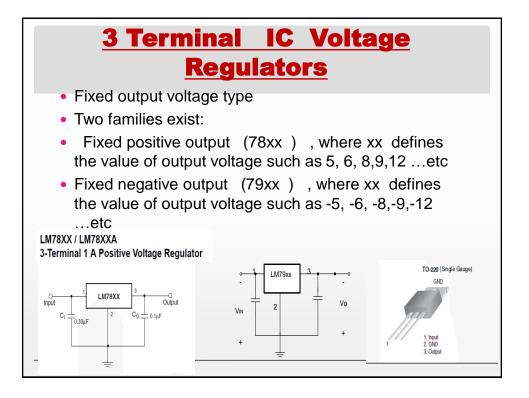
$$P_{_{Q1}}=V_{_{CE(MAX)}}*I_{_{E(MAX)}}$$

$$V_{CE(MAX)} = V_{IN(MAX)} - V_{O(MIN)} = 25 - 8 = 17 \text{ V}$$

$$I_{E(MAX)} = I_{R1} + I_{L(MAX)} = \frac{V_Z}{R_1} + I_{L(MAX)}$$
$$= \frac{8 \text{ V}}{20 \text{ k}\Omega} + 1 \text{ A} = 1.0004 \text{ A}$$

$$P_{Q1} = 17 \text{ V} * 1.0004 \text{ A} = 17.0068 \text{ W}$$





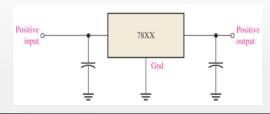
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Fixed Voltage Regulator (for reference only)

 The fixed voltage regulator has an unregulated dc input voltage V_i applied to one input terminal, a regulated output dc voltage V_o from a second terminal, and the third terminal connected to ground.

Fixed-Positive Voltage Regulator

 The series 78XX regulators are the three-terminal devices that provide a fixed positive output voltage.



2. Ground 3. Output

Fixed Voltage Regulator

Positive-Voltage Regulators in the 78XX Series

IC Part	Output Voltage (V)	Minimum V _i (V)
7805	+5	+7.3
7806	+6	+8.3
7808	+8	+10.5
7810	+10	+12.5
7812	+12	+14.5
7815	+15	+17.7
7818	+18	+21.0
7824	+24	+27.1

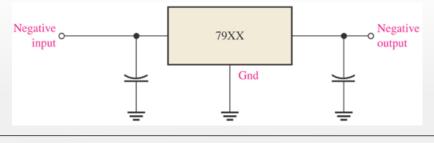
Vin must be higher than Vo by at least 2V for proper operation of the voltage regulator

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Fixed Voltage Regulator

Fixed-Negative Voltage Regulator

- The series 79XX regulators are the three-terminal IC regulators that provide a fixed negative output voltage.
- This series has the same features and characteristics as the series 78XX regulators except the pin numbers are different.



Fixed Voltage Regulator

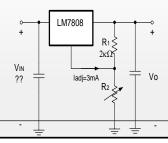
Negative-Voltage Regulators in the 79XX Series

IC Part	Output Voltage (V)	Minimum V _i (V)
7905	-5	-7.3
7906	-6	-8.4
7908	-8	-10.5
7909	-9	-11.5
7912	-12	-14.6
7915	-15	-17.7
7918	-18	-20.8
7924	-24	-27.1

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Adjustable Voltage Regulator based on 78xx family

- Find the minimum and maximum output voltage (Vo) for the following IC voltage regulator. Note that R2 is a variable resistor that can be varied from 0 to $3k\Omega$
- What is the range of values of Vin required for proper operation of the circuit
- What is the power dissipation of the LM7808 when Vo=Vo(min) and Vin=Vin(max) and load current =0.25A



Solution Voltage Regulators

$$\begin{split} &V_{o(MIN)} = V_{REG} = 8 \ V \ \ (when \ R_2 = 0 \ \Omega) \\ &V_{o(MAX)} = V_{R1} + V_{R2} = I_{REG} \big(R_1 \big) + \big(I_{REG} + I_{adj} \big) \big(R_2 \big) \\ &I_{REG} = \frac{V_{REG}}{R_1} \\ &V_{o(MAX)} \Big|_{R2 = 3k\Omega} = \frac{V_{REG}}{R_1} \big(R_1 + R_2 \big) + I_{adj} \big(R_2 \big) \\ &= \frac{8V}{2k\Omega} \big(2k\Omega + 3k\Omega \big) + 3mA. \big(3k\Omega \big) \\ &= \big(4mA \big). \big(5k\Omega \big) + 9V = 29V \end{split}$$

Voltage Regulators

Solution

 V_{IN} must be higher than Vo by at least 2 volts when Vo=8V, $V_{IN(MIN)}=8+2=10$ V when Vo=29V, $V_{IN(MAN)}=29+2=31$ V

Power Dissipation of LM7808:

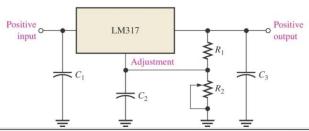
$$P_{\text{(LM7808)}} \cong (V_{\text{IN}} - V_{\text{o}}) * I_{\text{L}} = (31 - 8) * 0.25 = 5.75 \text{ W}$$

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Adjustable-Voltage Regulator

Adjustable-Voltage Regulator

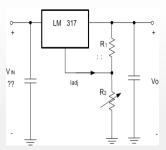
- Voltage regulators are also available in circuit configurations that allow to set the output voltage to a desired regulated value.
- The LM317 is an example of an adjustable-voltage regulator, can be operated over the range of voltage from 1.25 to 35 V



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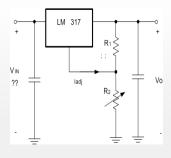
Voltage Regulators

- ladj=~ 50 uA (constant From data sheet)
- VREF=1.25 (always true for the 317 family)
- Vo=~ 1.25 35V
- Vo is defined by proper choice of R1 & R2
- V0=VR1+VR2
- VR1=VREF=IR1*R1
- IR1=IREF=VREF/R1
- VR2=(IREF+IADJ)*R2
- Vo=IREF*(R1+R2)+ladj*R2



Example

- Given R1=220 Ω ; R2=5k Ω potentiometer
- ladj=~ 50 uA (constant From data sheet)
- Find Vo(min) and Vo(max)
- Find range of Vin ?



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Voltage Regulators

$$\begin{split} &I_{\text{REF}} = \frac{V_{\text{REF}}}{R_{_{1}}} = \frac{1.25}{220\,\Omega} \\ &V_{_{O}} = I_{_{\text{REF}}} \Big(R_{_{1}} + R_{_{2}} \Big) + I_{_{\text{adj}}} \Big(R_{_{2}} \Big) \\ &V_{_{O(\text{MAX})}}|_{R2=5k\Omega} = \Big(26.66 + 0.25 \Big) = 29.91\,\text{V} \\ &V_{_{O(\text{MIN})}}|_{R2=0k\Omega} = V_{_{REF}} = 1.25\,\text{V} \end{split}$$

The input voltage must be higher than the output by at least 2 \ensuremath{V}

$$V_{IN(MIN)} \cong 1.25 + 2 = 3.25 \text{ V}$$

$$V_{IN(MAX)} \cong 29.91 + 2 = 31.91V$$